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## Facsimile Cover Sheet

To: Assistant Commissioner of Patents for Examiner Tim T. Vo Group Art Unit 3622	Facsimile No.: 703/746-5665
From: Amy Miller Legal Assistant to Kevin Goodsell	No. of Pages Including Cover Sheet: 2
<p>Message:</p> <p>Examiner Vo, <i>Interview on 12/2/03 1:30 pm</i></p> <p>Attached please find a copy of the proposed agenda that you requested per our telephone conversation.</p> <p>If you have any questions please do not hesitate to contact me.</p> <p>Thanks</p>	
Re: Application No. 09/671,065 Attorney Docket No: AUS9-2000-0452-US1	
Date: Friday, November 14, 2003	
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**Agenda For Telephone Conference With Examiner**

- I. Overview of Invention**
- II. Overview of Prior Art of Record**
- III. Rejection Under 35 U.S.C. 102**

- 1. Prior Art of Record does not teach or suggest a peripheral component interconnect to peripheral component interconnect bridge.
- 2. Examiner alleges that Anderson teaches this feature on the grounds that Anderson states "A Peripheral Component Interconnect or PCI Host bridge."
  - 2.1 This clearly states the bus bridge in question is a PCI Host bridge.
  - 2.2 In no way does this indicate a PCI to PCI bridge \*
  - 2.3 A PCI to PCI bridge is a separate component from a PCI Host bridge in a computer system
  - 2.4 A PCI Host bridge provides an interconnect between the processor and peripheral components while the PCI to PCI bridge provides a communication pathway between two PCI busses, allowing for additional PCI busses to be added to the system.
- 3. Each independent claim contains this feature and is thus allowable over Anderson